

United States Patent (15)

Perry

Patent Number: 4,807,277

Date of Patent: Feb. 21, 1989

[4] REMOTELY ACTIVATED SWITCHING APPARATUS

[75] Inventor: Steven B. Perry, Highlands, N.J.

[73] Assignee: Egant, Inc., Tinton Falls, N.J.

[21] Appl. No.: 51,043

[22] Filed: May 14, 1987

[31] Int. Cl. 4: H04M 1/04

[32] U.S. Cl.: 379/142, 379/143

[33] Field of Search: 379/1, 25, 102, 330

[36] References Cited

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CAN/11 1/1986 Japan 379/25

Primary Examiner—Charles L. Briggance

Assistant Examiner—Richard A. Eljery

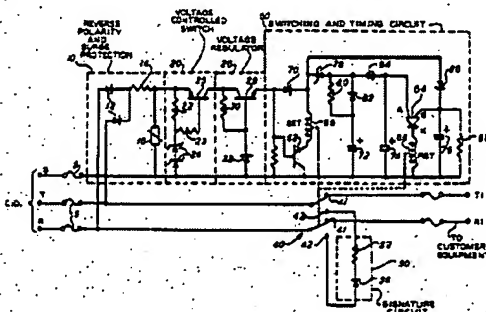
Attorney Agent or Firm—R. G. Rhodes, Jr.

[57] ABSTRACT

Apparatus for disconnecting and reconnecting equipment to a telephone line in response to a signal sent over

the telephone line. The apparatus includes a voltage threshold circuit, a voltage regulator circuit and a switching and timing circuit. The switching and timing circuit includes a first energy storage capacitor and a first discharge circuit. The switching and timing circuit further includes a second energy storage capacitor and a second discharge circuit and further includes a third energy storage capacitor and timing resistor. The first discharge circuit includes a first semiconductor switching device and a relay coil of a switching relay. The second discharge circuit includes a second semiconductor switching device and a second relay coil of the switching relay. When a signal is sent over the telephone line which exceeds the threshold level, it is regulated by the voltage regulator and charges up the first, second and third energy storage capacitors in an order such that the third capacitor charges first, the second capacitor next and the first capacitor last. Once the signal disappears from the telephone line, the first energy storage capacitor discharges into the first discharge circuit, thereby placing the switch into a first switching state and disconnecting the customer equipment. In this state, a test circuit, for example, may be connected to the telephone line to enable the central office to test the telephone line for faults. After a predetermined time delay determined by the timing resistor, the second energy storage capacitor into the timing resistor, the second discharge circuit is activated by a voltage differential between the second and third energy storage capacitors whereby the switch is switched from the first state back to the second state, thereby reconnecting the customer equipment, for example, a telephone, back to the telephone line.

20 Claims, 1 Drawing Sheet



US-PAT-NO: 4807277

DOCUMENT-IDENTIFIER: US 4807277 A

TITLE: Remotely activated switching apparatus

Detailed Description Text - DETX (2):

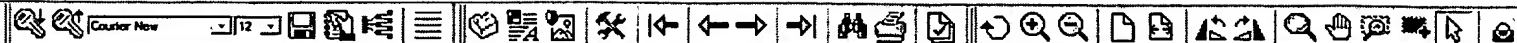
With reference now to the drawing, the apparatus of the present invention includes a first group of terminals connected to the incoming telephone line, i.e., to the central office (C.O.) telephone line. These terminals include a Tip (T), Ring (R) and ground (G) line. The ground line may be an earth ground. The incoming central office lines may be connected by fuses 5 to a reverse polarity and surge protection circuit 10. The output of the reverse polarity and surge protection circuit 10 is coupled to a voltage controlled switch circuit 20, which functions to go into conduction only if the input voltage exceeds a predetermined threshold voltage. The output of the voltage controlled switch circuit 20 is coupled to a voltage regulator circuit 26, which functions to regulate the voltage from the voltage controlled switch circuit 20 to a predetermined output voltage. The output voltage from voltage regulator circuit 26 is then coupled to switching and timing circuit 60, which controls switch 40, for example, a relay. Relay 40 may comprise a double pole/double throw relay having one pair of stationary contacts 41 connected to the customer equipment (T1 and R1) and having the other pair of stationary contacts 42 connected to signature circuit 50.

☐ Details ☒ Text ☐ Image ☐ HTML ☐ KWIC

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D-C motor system	318/
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L15: (47) reverse adj... | US 4510431 | Tag: S | Doc: 35/47 | Full |

L15: (47) reverse adj... | US 4510431 A | Tag: S | Doc: 35/47 | Format : KWIC |

United States Patent (19)

Winkler

(11) Patent Number: **4,510,431**
(45) Date of Patent: **Apr. 9, 1983****D.C. STEPPED-UP VOLTAGE
TRANSFORMERLESS BATTERY CHARGER**(76) Inventor: **Harry L. Winkler, P.O. Box 432,
Pinebluff, La. 71350**(21) Appl. No.: **333,139**(22) Filed: **Feb. 25, 1983****Related U.S. Application Data**(43) Continuation of Ser. No. 143,726, Jan. 21, 1980, aban-
doned.(31) Int. Cl. **H02M 7/00**(32) U.S. Cl. **328/24; 328/39**(33) Field of Search **361/21; 310/1, 39****References Cited****U.S. PATENT DOCUMENTS**3414,581 12/1971 Fries
3,775,839 11/1973 Carlsen
4,226,967 11/1978 Hayman

4,311,034 6/1980 Copd

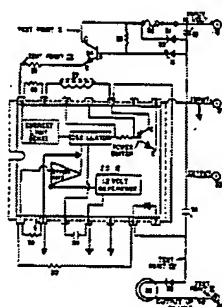
FOREIGN PATENT DOCUMENTS

1119491 6/1978 United Kingdom

Priority Documents—William M. Beha, Jr.,
Attorney—Eugene—Judson H. Jones**ABSTRACT**

A battery charger that takes energy from a battery and steps-up the voltage to charge another battery. The direct current going into the charger's circuit goes through the voltage regulator and then to a switching voltage regulator (the integrated circuit) and a few external components which generates the energy to charge the output capacitor. The output capacitor voltage is controlled by a relative voltage divider network, and energy stored in the output capacitor is transferred through a diode and a current limiter to charge the battery.

1 Claim, 1 Drawing Figure



US-PAT-NO: 4510431
DOCUMENT- US 4510431 A
IDENTIFIER:
TITLE: D.C. Stepped-up voltage transformerless battery charger

Detailed Description Text - DETX (2):

This battery charger is mainly designed to charge 12 and 15 volt nickel-cadmium batteries for hand held transceivers. The battery charger operates from 12 volts direct current with negative ground. With positive power going into the charger circuit, it goes through the switch (21) and through the fuse (22). The fuse (22) is used to protect the circuit of the charger in case something goes wrong, also for protection from reverse polarity. A rectifier type diode (20) the anode is connected to the ground (17) and the diode (20) cathode is connected to the far side of the fuse (22). When the switch (21) is on and the diode (20) receives reverse polarity, it will short out the fuse (22) thus causing it to blow. This protects the regulator transistor (24) as well as the Integrated Circuit (15) . . . from here on, the Integrated Circuit (15) will be referred to as I.C. (15).

Claims Text - CLTX (4):

a circuit protector comprising a fuse and a first rectifier, means for connecting one end of said fuse to said power switch and means for connecting said rectifier to be poled to blow said fuse upon receiving reverse polarity of said voltage source;

Details Text Image HTML KWIC

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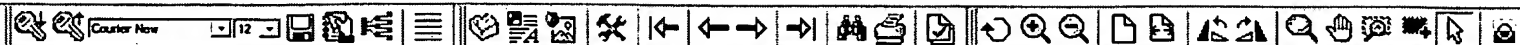
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L15: (47) reverse adj... | US 3956683 | Tag: S | Doc: 40/47 | "Full" |

United States Patent (11)

(11) 3,956,683

Popp

(45) May 11, 1976

(54) TAPER TYPE OF BATTERY CHARGER

(75) Inventor: Ralph Popp, Pittsburgh, Pa.

(73) Assignee: Westinghouse Air Brake Company, Westvale, Pa.

(22) Filed: Mar. 6, 1974

(21) Appl. No. 448,459

(52) U.S. Cl. 320/13; 320/79;

321/18

(51) Int. Cl. 3 H02J 7/18

(54) Field of Search 320/13-24,

320/79, 40, 51, 31; 321/18, 19

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Primary Examiner—J. D. Miller

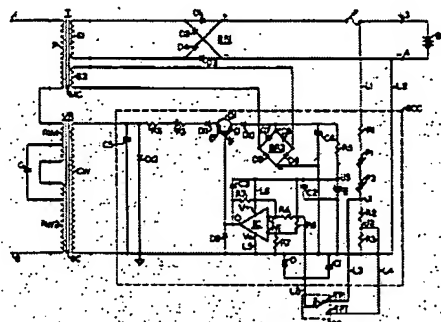
Assistant Examiner—Robert J. Mickey

Attorney, Agent, or Firm—J. B. Sock; R. W. McIntire, Jr.

[57] ABSTRACT

This disclosure relates to an automatic taper charging battery charger having a power transformer and a series connected variable reactor, a pair of rectifiers powered by the transformer, a pair of output terminals coupled to one of the rectifiers, a sensing and control circuit powered by the other rectifier and responsive to the voltage of a battery connected to the output terminals for controlling the impedance of the variable reactor and in turn regulating the voltage developed across the power transformer and thereby controlling the current charging rate supplied to the battery.

8 Claims, 1 Drawing Figure



US-PAT-NO: 3956683

DOCUMENT-IDENTIFIER: US 3956693 A

TITLE: Taper type of battery charger

Detailed Description Text - DETX (11):

As mentioned above, reverse polarity protection positively ensures that the battery charger is not damaged by burn out or the like by the diodes of bridge rectifier BR1 and fuse F. If the battery B is inadvertently connected to the reverse manner from that shown in the drawings, the four diodes of the bridge rectifier BR1 immediately conduct and short circuit the battery B through the fuse element F. The short circuiting causes the fuse F to blow thereby immediately breaking the circuit so no resulting impairment will occur to the various current sensitive elements of the charger. In addition, the diode D protects the operational amplifier IC while the diode DiO protects the transistor Q1 against damage and destruction on inadvertent reverse connection of the battery.

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apparatus for 236/
and controlling
of battery 320/apparatus for 137/
and controlling



L15: (47) reverse adj... | US 5488872 | Tag: S | Doc: 23/47 | Format: KWIC

United States Patent (19)

McCormick

(11) Patent Number: **5,488,872**
 (45) Date of Patent: **Feb. 6, 1996**

(54) SYSTEM AND METHOD FOR LOAD SENSING

(75) Inventor: **Peter McCormick, Dallas, Tex.**

(73) Assignee: **EDA Systems, Inc., Dallas, Tex.**

(21) Appl. No.: **79,491**

(22) Filed: **Jun. 17, 1993**

(51) Int. Cl. ⁸ **G01L 1/00**

(52) U.S. Cl. **73/634; 73/635**

(53) Field of Search **73/634, 635, 636**

(56) References Cited

U.S. PATENT DOCUMENTS

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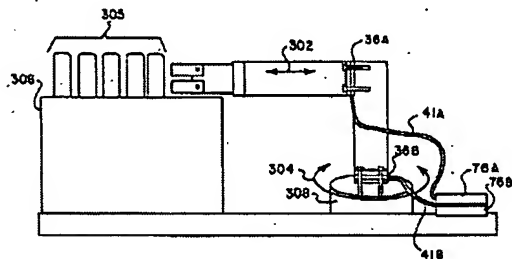
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 4,356,423 (10/1980) Ochs et al.
 4,356,423 (10/1980) Ochs et al.
 4,356,423 (10/1980) Ochs et al.

Primary Examiner—Richard B. O'Brien, Jr.
 Assistant Examiner—B. Blagel
 Attorney, Agent, or Firm—David V. Thompson

(57) ABSTRACT

An load sensing system employs a piezoelectric sensor and digital processor for providing accurate sensing of load, collisions, and vibrations.

7 Claims, 10 Drawing Sheets



US-PAT-NO: 5488872
 DOCUMENT-IDENTIFIER: US 5488872 A
 TITLE: System and method for load sensing

Detailed Description Text - DETX (6):

A power supply 43 supplies the applied voltage (V) on terminals 42, through protective fuse 44, and through the reverse polarity protection diode 46. The cathode of the protection diode 46 provides the applied voltage V to the power conditioning circuit 30 which includes a zener diode (not shown) and a regulator (not shown) for regulating and dropping the voltage to logic compatible levels. Many expedients are known for the power conditioning circuit 30, the exact configuration not being necessary for the understanding of the present invention.

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22	<input type="checkbox"/>	<input type="checkbox"/>	US 5563821 A	19961008	22	rotor employing permanent			
23	<input type="checkbox"/>	<input type="checkbox"/>	US 5488872 A	19960206	17	Semiconductor memory device having a program circuit for	365/		
24	<input type="checkbox"/>	<input type="checkbox"/>	US 5468680 A	19951121	18	System and method for load sensing	73/8		
4	<input type="checkbox"/>	<input type="checkbox"/>				Method of making a three-terminal fuse	438/		

U.S. Patent

June 20, 1995

Sheet 3 of 12

5,426,771

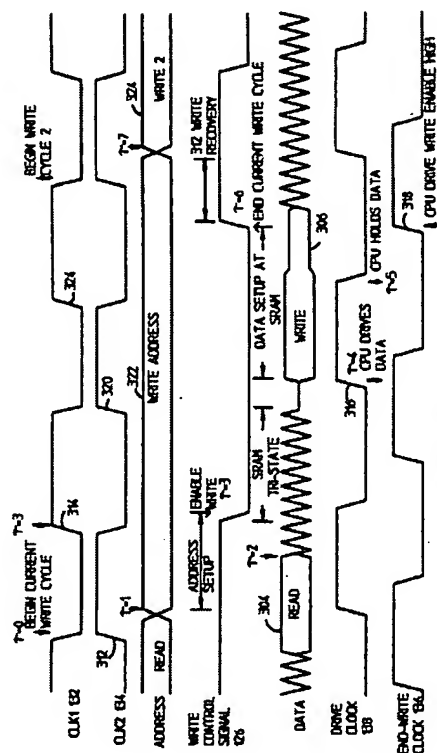


FIG 3

US-PAT-NO: 5426771

DOCUMENT- US 5426771 A

IDENTIFIER:

See image for Certificate of Correction

TITLE:

System and method for performing high-speed cache memory writes

Detailed Description Text - DETX (68):

When new data is driven by CPU 110, the drive FETs switch the output signals. The hold circuit then maintains this signal on the bus during the data hold period. The hold circuit essentially follows the output. The higher current drive FETs can easily overdrive the hold circuit, thus switching the output. The hold circuit then holds the new value. An advantage of holding data on data bus 124 using keeper FETs is that chip enable and output enable signals are not required. This is because the high current FETs that originally drive data on to data bus 124 in step 214 are turned off and only the keeper FETs hold the data. New data can be driven on to data bus 124 by either CPU 110 or by SRAM 120 without turning off the hold circuit FETs. Since the drive FETs have already been turned off, situations where SRAM 120 drivers are fighting CPU 110 drivers will never arise.



(10) Patent No.: US 6,384,875 B2
(45) Date of Patent: May 7, 2002

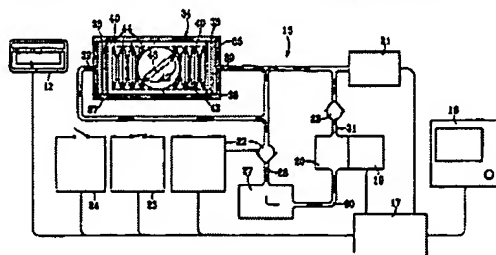
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| 5,143,487 | A | 47,900 | Naka | |
| 5,184,873 | A | 87,900 | Tachio | |
| 5,146,223 | A | 87,900 | Kawano | |
| 5,175,772 | A | 131,600 | Huang | 5,465,721 |
| 5,181,887 | B1 | 127,200 | Kawano | 5,465,721 |
| 5,184,873 | B1 | 272,200 | Naka | 5,465,721 |

Primary Examiner—Steven E. King
Assistant Examiner—Paul A. DeJ
(74) Attorney, Agent, or Firm—Small, Larch, LLP
(57) ABSTRACT

ABSTRACT

- The present invention relates to a video machine display positioning system of the type in which space is in perspective-divergent arrangement and includes a locating in which video images are usually positioned. For operation between a standard position and an extended position, there is a rotary member provided through a hydraulic system which includes a mechanical linkage from the screen to a linear member to mechanical motion actuates substantially which is in turn actuated by a hydraulic system that includes a cylinder, one extremity opposed, hollowed plunger, an internal spring positioned within the cavity formed by the hollow plunger, a hydraulic circuit including a hydraulic oil reservoir, an electric motor that controls a hydraulic pump, a solenoid valve and an on over pressure switch.

16 Claims, 11 Drawing Sheets



Detailed Description Text - DETX (29):

Also, it is preferred that the control system be configured so that a retract command must be given to reset the over-pressure latch before an extend command will be recognized in the case that the over pressure switch opens and the screen retracts. In the present embodiment it is envisioned that reverse polarity protection is provided through conventional circuitry but that no fuse protection is incorporated. Specific board layout, connecting wire descriptions, junction box locations and configurations are within the skill of the art in this field and are chosen so that the system will work for its intended purpose of positioning the screen hydraulically within the principle of the invention and in the specific application chosen.



US 20050049107A1

(10) United States

(12) Patent Application Publication (30) Pub. No.: US 2005/0049107 A1
Willmot et al. (40) Pub. Date: Mar. 3, 2005

(50) TRANSMISSION SYSTEM

(57) ABSTRACT

(70) Inventors: Eric P. Willmot, Mount Eliza (AU);
Lance P. Pagan, Oremobyras (AU)Correspondence Address:
MICHAEL BEST & FRIEDRICH, LLP
180 E WISCONSIN AVENUE
MILWAUKEE, WI 53202 (US)

(71) Appl. No.: 10/446,380

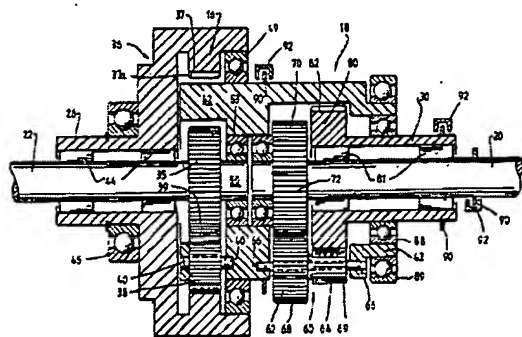
(72) PCT Filed: Dec. 10, 2001

(80) PCT No.: PCT/AU01/01589

Publication Classification

(51) Int. Cl. F16H 57/06
(52) U.S. Cl. 475/335

Transmission systems are disclosed which include a dual screwhead system having an output screwhead (70) and a control screwhead (80). A planet cage (62) is arranged around the dual screwhead system and includes a planet gear (64) in mesh with the screwhead (70) and a planet gear (64) in mesh with the screwhead (80). The planet gears (62, 64) are coupled to one another. The drive ratio of the transmission is controlled by controlling the cage (62) or the control screwhead (70). The speed of rotation of the input (22), output (20) and control screwhead (70) are sensed by sensors (76) and control signals are generated to control a control device to thereby control the rotation of planet cage (62) or the control screwhead (80) to thereby set the drive ratio of the transmission. The control devices can include a motor, one or more magnetic powder clutches, a variable controlled system and a mechanical pitch transfer gear system.



DOCUMENT-IDENTIFIER: US 20050049107 A1

TITLE: Transmission system

Detail Description Paragraph - DETX (141):

[0295] With reference to FIG. 24, the controller has a pair of field effect transistors 700 and 701 which are connected in parallel with one another. Power is supplied to the field effect transistors from a battery 703. A diode 704 is provided to protect the circuitry should a battery of a higher voltage than required be used or the battery connected in reverse polarity. A fuse 705 is connected between the diode 704 and the battery so that should the voltage supplied by the battery be too high or the battery connected in reverse polarity, the fuse 705 will burn out to thereby shut off power supplied to the circuitry shown in FIG. 24.

Reid et al.

(21) Patent Number: 5,636,863
(45) Date of Patent: Jun. 10, 1997

[54] VEHICLE STEERING COLUMN CONTROL SYSTEM

[75] Investors: Scott A. Reid, Brookfield Park G.
Estate, Germantown; James E. Hannon,
Oak Creek; Ruth E. Ebbell,
Milwaukee; Erich Rahm, West Bend,
all of Wis.

[73] Assignee: Easton Corporation, Cleveland, Ohio

[21] Appl. No. 233,633

203 Filed: Apr 26,

U.S. CI. _____ 224/736; 242/731; 340/536

(14) Field of Search _____ 230/734, 735
230/731: 100/710 1: 430/35 154: 90/4072

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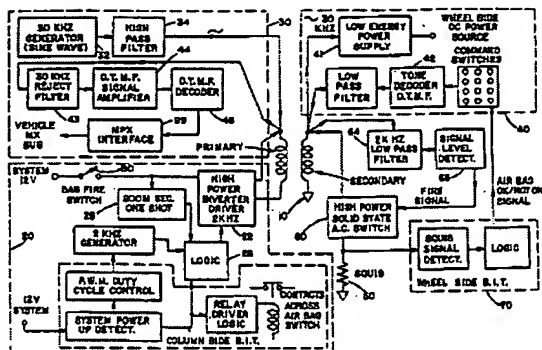
4/24/73 12/19/73 Buzen et al. _____ 307/20.1
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5500773	6/2/64	_____	_____	907350.0

Primary Editor—Eric D. Coltrane

57 ABSTRACT

A control circuit is provided for transferring power and communications signals through a wireless coupling device to a vehicle steering column. The device can comprise a rotary transformer having a primary and secondary magnetic structure magnetically associated with the wheel side and the steering column. The steering column is supplied with direct transformer high voltage signals, signal source firing or other signals from the column side to the wheel side. A low energy power circuit provides a signal from the column side to the wheel side suitable for operating isolated wheel side electronics, such as cruise control and the like. A tone encoder and decoder circuit communicates low level control signals externally to the signaling wheel side commands from the wheel side to the column side. The low energy power circuit and the tone encoder circuit operate discontinuously and continuously without signal interference interaction.

23. Clinton, 6 Dewdney Street

US-PAT-NO: 5636263

DOCUMENT-IDENTIFIER: US 5636863 A

TITLE: Vehicle steering column control system

Detailed Description Text - DETX (9):

The H-drive alternately switches the primary with forward and reverse polarity square wave signals equal to the system DC power applied to it. With a conventional 12 volt vehicle system, this is the equivalent of applying a plus and minus 12 volt square wave (24 volts peak-to-peak) onto the transformer 10, yet only using a single ended 12 volt supply. Accordingly, primary currents will exceed 20 amperes and secondary voltages and resultant currents into the squib are sufficient to fire an air bag.

Detailed Description Text - DETX (10):

In the embodiment shown in FIG. 2C, the square wave signal is initially generated by a 2 khz generator circuit 24 (TL 494 integrated circuit), especially designed for inverter and PWM power supply applications. Two alternating square wave signal outputs are generated (pins 9 and 10) to be used to drive the H-drive circuit 22. The H-drive output is comprised of four high current FET power transistors 91, 92, 93, 94, two P-channel types and two N-channel types. For operation, for instance, transistors 91 and 94 are to be turned on to conduct current through the transformer primary in one direction. In effect, one side of the winding is connected to plus 12 volts and the other side to ground. On the next half-cycle, this is reversed by turning off transistors 91, 94 and instead turning on transistors 92, 93, and in effect reversing the polarity on the transformer. The generator circuit 24 is operated in a mode that allows a brief "OFF" period between polarity reversals to assure that two FETs on one side of the bridge are not simultaneously turned on, which could cause a short across the main supply to ground (i.e., transistors 91 and 93 are never on simultaneously, or transistors 92, 94 either). Logic circuitry 26 between the generator 24 and the H-drive 22 is used to switch the H-drive 22 from an inoperational state (normal system operation) to a fire condition. In other words, during normal operation of the steering control system, the H-drive 22 should not be operating in the high power inverter mode. Rather, a transistor 94 is the only FET biased on, and this is done to ground at one end of the transformer primary, which is necessary for normal operation. The other three FETs 91, 92, 93 are held in an OFF state

Details Text Image HTML KWIC

21	<input type="checkbox"/>	<input type="checkbox"/>	US 3929436 A	19751230	11	circuit for an exciter field Rotary electrostatic precipitator	96/5
22	<input type="checkbox"/>	<input type="checkbox"/>	US 3851232 A	19741126	18	ELECTRIC VEHICLE PROPULSION SYSTEM	318/
23	<input type="checkbox"/>	<input type="checkbox"/>	US 3786264 A	19740115	10	HIGH SPEED LIGHT DETECTOR AMPLIFIER	250/

United States Patent (19)

Bertalan et al.

US 6130487 A
 (11) Patent Number: 6,130,487
 (45) Date of Patent: Oct. 10, 2000

(54) ELECTRONIC INTERFACE AND METHOD FOR CONNECTING THE ELECTRICAL SYSTEMS OF A TRUCK AND TRAILER

(75) Inventors: Richard P. Bertalan, Anthony Law, E. Thomas, LaCrosse, Wis. M.; Ortel, Bruce; Menden, Jeffrey E.; Berglund, Douglas, all of Wis.

(73) Assignee: PACCAR Inc., Bellevue, Wash.

(21) Appl. No.: 09/233,314

(22) Filed: Feb. 19, 1999

(51) Int. Cl.⁷ B60D 1/00

(52) U.S. Cl. 307/10.1; 342/310.01; 340/431.1; 340/735; 340/923.06

(57) Field of Search 307/10.1, 9.1; 340/923.06, 538, 431, 310.01; 340/431.1, 656; 180/14.2

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U.S. PATENT DOCUMENTS

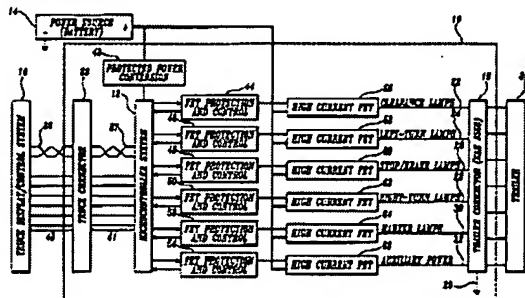
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Primary Examiner—Abner W. Paladino
 Attorney Agent, or Firm—Christensen O'Connor Johnson & Kindness PLLC

ABSTRACT

An electrical interface for connecting a truck electrical system to a trailer electrical system. The electrical interface includes a solid-state switching device for selectively connecting a power source to the truck electrical system to a circuit in the trailer electrical system. If a fault is detected in the provision of power to the circuit in the trailer electrical system, the solid-state switching device reduces the flow of power or disconnects the power source from the circuit in the trailer electrical system. A microcontroller system connected to the truck electrical system transmits control signals to the solid-state switching device to control the flow of power from the power source to the circuit in the trailer electrical system in accordance with instruction signals received from the truck electrical system. The solid-state switching device includes a switching switch connected between the power source and the circuit in the trailer electrical system and a transistor driver circuit connected to the microcontroller system to control the operation of the switching switch. The microcontroller system includes a processor and a transceiver that receives data transmitted between the processor and the truck electrical system. The electrical interface may also include a diagnostic board connector for connecting an external circuit, such as a computer, to the electrical interface that enables the processor to communicate data with the circuit in the trailer electrical system using an existing electrical connection to an output of the electrical interface.

24 Claims, 18 Drawing Sheets



a 7-pin SAE J560 connector. One pin of the J560 connector is connected to ground via line 20. The other six pins of the J560 connector are connected to high-current field-effect transistors (FETs) 56, 58, 60, 62, 64, and 66 via lines 22, 24, 26, 28, 30, and 32 to provide an electrical connection to the following respective circuits in the trailer 34: clearance lamps, left-turn signal lamps, stop/brake lamps, right-turn signal lamps, marker lamps, and auxiliary power circuit.

Detailed Description Text - DETX (8):

The power conversion circuit 42 includes a diode D1 to protect the circuit from reverse polarity of the inputs of the power source 14. The power conversion circuit 42 also includes an inductor L1 and capacitors C1 and C2 to filter higher frequency noise on the voltage input from the power source 14.

Detailed Description Text - DETX (12):

The microcontroller system 12, an embodiment of which is described in more detail below in reference to FIGS. 3A and 3B, receives instruction signals from the truck display/control system 18 by way of the data link 37 and/or individual signal lines 41 as shown in FIG. 1. FIG. 3A is a schematic drawing of a first portion of the microcontroller system 12 showing the microcontroller aspect of the microcontroller system 12. FIG. 3B is a schematic drawing of a second portion of the microcontroller system 12 showing the data communication aspect of the microcontroller system 12. According to instruction signals received from the truck display/control system 18, the microcontroller system 12 sends control signals to one or more FET protection and control circuits 44, 46, 48, 50, 52, and 54 to permit or interrupt the flow of current from the power source 14 through high-current FETs 56, 58, 60, 62, 64, and 66 to the trailer connector 16. The FET protection and control circuits 44, 46, 48, 50, 52, and 54, and the corresponding high-current FETs 56, 58, 60, 62, 64, and 66, are described in more detail below in reference to FIGS. 4-9.

Detailed Description Text - DETX (14):

Control lines labeled Control1-Control16 convey control signals from output pins on the processor U3 to the FET protection and control circuits 44, 46, 48, 50, 52, and 54 to indicate when to turn on and off the corresponding high-current FETs 56, 58, 60, 62, 64, and 66. As will be better understood from the discussion below regarding the FET protection and control circuits 44, 46, 48, 50, 52, and 54, faults detected in providing power to the trailer 34 are reported to the processor U3 through lines Fault1-Fault6. The processor U3 may then forward information

Details	Text	Image	HTML	KWIC
17	US 4791349 A	19881213	17	Electric power system 323/
18	US 4758937 A	19880719	21	DC-DC converter 363/
19	US 4616166 A	19861007	25	Electric power system for starting a large rotatable 318/

(15) United States

(12) Patent Application Publication (19) Pub. No.: US 2004/0009075 A1
Meza et al. (10) Pub. Date: Jan. 15, 2004(54) PUMP AND PUMP CONTROL CIRCUIT
APPARATUS AND METHOD(70) Inventors: Humberto V. Meza, Trinita, CA (US);
Nikolai Bessire Gendin, Anaheim, CA
(US); Qing Liang Truong, West
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(21) Appl. No.: 10433,874

(22) Filed: Jan. 4, 2003

Related U.S. Application Data

(67) Continuation-in-part of application No. 09/994,378,
filed on Nov. 25, 2001, now Pat. No. 6,523,245.

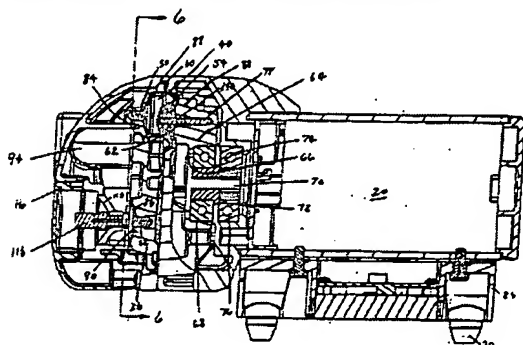
Publication Classification

(51) Int. Cl. F04B 49/10
(52) U.S. Cl. 417/332; 417/44.11

(57)

ABSTRACT

A method and apparatus for a pump and a pump control system. The apparatus includes a pump assembly formed in a diaphragm and coupled to the diaphragm by a membrane. The membrane has a bottom surface angled with respect to a top surface of the diaphragm. The apparatus also includes an outlet port positioned longitudinally with respect to the perimeter of an outlet chamber. The apparatus further includes a non-mechanical pressure sensor and a temperature sensor coupled to a pump control system. For the method of the invention, the microcontroller provides a pulse-width modulation control signal to an output power stage in order to selectively control the power provided to the pump. The control signal is based on the pressure within the pump, the current being provided to the pump, the voltage level of the battery, and the temperature of the pump.



DOCUMENT-IDENTIFIER: US 20040009075 A1

TITLE: Pump and pump control circuit apparatus and method

Detail Description Paragraph - DETX (68):

[0125] As shown in FIG. 24, a first power temperature control (PTC) device 519 and a second PTC device 521 can be connected in series with the connection 518 to act as **fuses in order to protect against a reverse in polarity**. In some embodiments, a first battery cable (e.g., represented by the connection 519) can be connected to a positive input of the input power stage 504 and a second battery cable (e.g., represented by the connection 520) can be connected to a negative input of the input power stage 504. The first battery cable can be designed to connect to the positive terminal of the battery and the second cable can be designed to connect to the negative terminal of the battery. However, the PTC devices 519 and 521 can protect against reverse polarity. If the first battery cable is initially connected to the negative terminal of the battery and the second battery cable is initially connected to the positive terminal of the battery, the electronics of the pump control system 500 will not be harmed. When the first and second cables are switched to the proper battery terminals, the pump 10 will operate normally.

Details Text Image HTML KWIC

	P	I	A1				
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010043455 A1	20011122	18	Video display positioning system	361/
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010017755 A1	20010830	12	Semiconductor integrated circuit provided with input	361/
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6870196 B2	20050322	11	Series/parallel OLED light source	257/

(1) United States

(2) Patent Application Publication (3) Pub. No.: US 2002/0109952 A1
Rapinski et al. (4) Pub. Date: Aug. 13, 2002(5) HIGH VOLTAGE BATTERY CUTOFF
CIRCUIT FOR A MOTOR VEHICLE
ELECTRICAL SYSTEM

(6) Int. Cl. H01M 3/14

(7) Inventor: Robert S. Rapinski, Kokomo, IN
(US); Alexander C. Lewison, Kokomo,
IN (US); Debra Ann, Kokomo, IN
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(8) ABSTRACT

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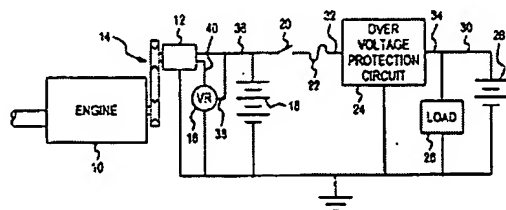
(9) Appl. No.: 09/782,711

(10) Filed: Feb. 12, 2001

Publication Classification

(11) Int. Cl. H02H 1/18

An improved over-voltage protection circuit for a motor vehicle electrical system includes an over-voltage responsive circuit for momentarily disconnecting the vehicle storage battery and alternator from vehicle electrical loads, and an auxiliary storage battery for supplying a safe operating voltage to the electrical loads during the momentary disconnection. The over-voltage responsive circuit includes a MOSFET device that controls the vehicle storage battery and alternator to the electrical loads and auxiliary storage battery, and a voltage responsive circuit that turns the MOSFET device off to decouple the electrical loads and auxiliary storage battery from the vehicle storage battery and alternator so long as an over-voltage condition is detected.

DOCUMENT -
IDENTIFIER:
TITLE:

US 20020109952 A1

High voltage battery cutoff circuit for a motor vehicle
electrical system

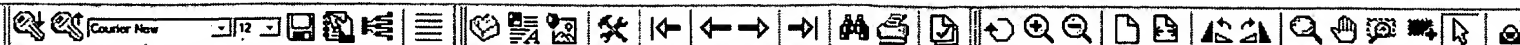
Detail Description Paragraph - DETX (4):

[0008] The over-voltage protection circuit 24, shown in detail in FIG. 2, operates in the event of a specified over-voltage on line 32 to effectively decouple lines 32 and 34, isolating the load 26 and auxiliary storage battery 28 from the alternator 12 and main storage battery 18. In such event, the auxiliary battery 28 provides power to load 26 via line 30, and the load 26 is protected from over-voltage damage, and loss of function due to automatic shut-down is prevented. Referring to FIG. 2, the over-voltage protection circuit 24 includes a P-channel MOSFET 50 coupling line 32 to line 34 through its source-to-drain circuit, a pull-down resistor 52 connected between the MOSFET (gate terminal 54 and ground for normally biasing the MOSFET 50 to a conductive state, and a voltage responsive circuit 56 for biasing MOSFET 50 to a non-conductive state when the voltage with respect to ground on line 32 exceeds a predefined threshold. The voltage responsive circuit 56 includes a zener diode 58 and resistor 60 coupled in series between line 32 and ground potential, a NPN transistor 62 having its base coupled to a junction 64 between zener diode 58 and resistor 60, and a PNP transistor 66 having its emitter-collector circuit coupled across the gate-to-source circuit of MOSFET 50. The zener diode 58 is ordinarily reverse biased, and the resistor 60 maintains the transistors 62 and 66 in non-conductive states. However, when the voltage on line 32 exceeds the breakdown voltage of zener diode 58 (which may be 18 volts, for example), a current path is established through the resistor 60, and the resulting voltage at junction 64 biases transistor 62 to a conductive state. This establishes a current path through the emitter-base circuit of transistor 66, the collector-emitter circuit of transistor 62 and resistor 68, biasing transistor 66 conductive to place a low impedance path between the gate and source terminals of MOSFET 50 to bias MOSFET 50 to a non-conductive state. Finally, the over-voltage protection circuit 24 also includes a second zener diode 70 connected between line 32 and ground potential for limiting the peak voltage applied to the aforementioned circuit elements, and for establishing a low impedance path through fuse 22 and battery 18 in the event that a reverse polarity is applied to battery 18 during jump starting; for example, zener diode 70 may have a breakdown voltage of approximately 35-volts.

Details Text Image HTML KWIC

9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	A1	US 20010043455	20011122	18
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	A1	US 20010017755	20010830	12
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	B2	US 6870196	20050322	11

Video display positioning system	361/
Semiconductor integrated circuit provided with input	361/
Series/parallel OLED light source	257/



Patent Application Publication Nov. 29, 2001 Sheet 2 of 21 US 2001/0045355 A1

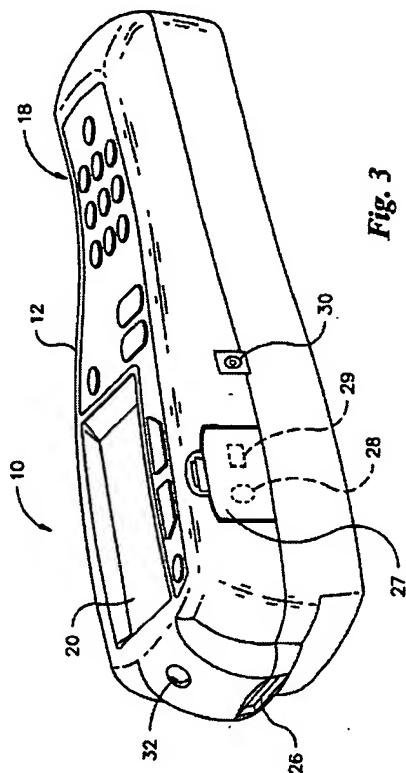


Fig. 3

DOCUMENT-IDENTIFIER: US 20010045355 A1

TITLE: Medical diagnostic system

Detail Description Paragraph - DETX (21):

[0045] The main source of power for the instrument 10 is a battery pack 524. In the present embodiment, the battery pack 524 is comprised of six (6) series connected nickel-metal hydrid (NiMH) batteries to provide a nominal 7.2 volt output source. The use of nickel-metal hydrid technology allows for high energy density and quick recharge times. However, other types of batteries such as nickel-cadmium (NiCD) and lithium-Ion (LiIon) and others known to those skilled in the art could be used. The instrument 10 also includes an intelligent fast charge controller 526 which functions to recharge the battery pack 524, typically in two hours or less and continuously monitors battery temperature using a sensor (not shown) embedded within the battery pack. In the present embodiment, the intelligent battery charger comprises a Maxim MAX 712 integrated circuit. Other intelligent battery charger circuits may be used if desired. If the battery pack temperature is too high or too low, the intelligent charger 526 stops the fast charging operation until a safe battery pack temperature level is reached. A self resetting fuse (not shown) is also embedded within the battery pack 524 to provide enhanced safety. The battery charger 526 is activated whenever an accompanying AC adapter wall pack (not shown) is connected to the instrument 10 through the battery charger connection 30 (FIG. 3) to provide power to the instrument 10 and to permit normal use of the device 10 during recharging of the battery pack 524. A second fuse (not shown) is also provided at the input connection for the wall pack and both the battery pack 524 and the wall pack connections have reverse polarity protection.

Details	Text	Image	HTML	KWIC
9	<input checked="" type="checkbox"/> A1	US 20010043455 20011122	18	Video display positioning system
10	<input checked="" type="checkbox"/> A1	US 20010017755 20010830	12	Semiconductor integrated circuit provided with input
11	<input checked="" type="checkbox"/> B2	US 6870196 B2 20050322	11	Series/parallel OLED light source



US06833683B2

(12) United States Patent
Winkler

(15) Patent No.: US 6,833,683 B2
(45) Date of Patent: Dec. 21, 2004

(54) UNIVERSAL BATTERY CHARGER
APPARATUS

(56) Reference Cited
U.S. PATENT DOCUMENTS

6,806,621 B1 * 4,200,333 A1 10/79

* cited by examiner

Primary Examiner—Lawrence Lok

(57) ABSTRACT

(70) Inventor: Harry L. Winkler, P.O. Box 632,
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(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 57 days.

(71) Appl. No.: 10/447,733

(72) Filed: Apr. 7, 2003

(65) Prior Publication Data

US 2004/019658 A1 Oct. 7, 2004

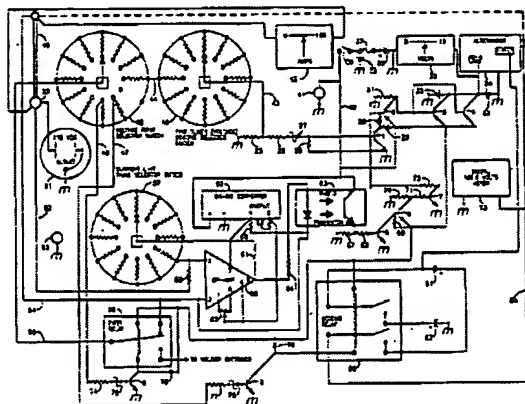
(51) Int. Cl. H02J 7/00

(52) U.S. Cl. 320/189; 320/107

(56) Field of Search 320/110, 115, 121, 123

A Universal Battery Charger Apparatus designed to be used
with a small gas engine equipped with a small alternator,
capable of generating the charge level of a small 12 volt
battery. Said battery supplies power for the circuit of said
battery charger. Said engine drives a second alternator. Said
battery charger has a wide range of assembled output volt-
ages as well as 12 different current limiting capabilities
making possible use of not only a battery charger for
charging different voltages of external batteries, but also a
DC arc welder under a 115 volt DC power supply, all
within its own framework, thus making said apparatus
portable.

11 Claims, 2 Drawing Sheets



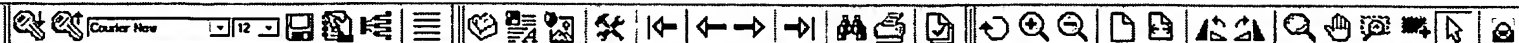
US-PAT-NO: 6833683
DOCUMENT-IDENTIFIER: US 6833683 B2
TITLE: Universal battery charger apparatus

Detailed Description Text - DETX (2):

An explanation of the universal battery charger's circuit is based with a 12 volt battery #36 installed, with the 12 volt-power switch #39 turned ON. A diode #38 is wired across one end of a fuse #37 and to the ground. Said diode is poled to blow said fuse in the event of reverse polarity. As seen in FIG. 2, transistors #2,3,4 and 6 are PNP type, with their emitters connected to the 12 volt-lead #40. Transistors #1,5,7,8 and 9 are NPN type, with their emitters connected to ground. As seen in FIG. 2, the collectors of both power transistors are connected to the field terminal of the alternator #34. Said output power transistors #3 and #4 are a matched pair and are wired to produce full output voltage to said field of said alternator #34 until said transistors receive a different command from either the voltage sensor or the current limiter. A resistor #31 with one end connected to ground, the other end connected to the bases of said power transistors #3 and #4, thus making a negative bias for said power transistors. Two diodes #32 and #33 with anode ends connected to ground and cathodes connected to the collector's of said power transistors #3 and #4. Said diodes are used to protect said power transistors from inductive spikes caused by the field windings of said alternator #34.

Details Text Image HTML KWIC

11			A1						
11			US 6870196 B2	20050322	11			circuit-provided with input	
12			US 6833683 B2	20041221	8			Series/parallel OLED light	257/
13			US 6606227 B2	20030812	5			source	
								Universal battery charger	320/
								apparatus	
								High voltage battery cutout	361/
								circuit for a motor vehicle	



L15: (47) reverse adj... | US 6256185 | Tag: S | Doc: 15/47 | "Full" |

L15: (47) reverse adj... | US 6256185 B1 | Tag: S | Doc: 15/47 | Format : KWIC

(12) United States Patent Mallier

(16) Patent No.: US 6,256,185 B1
(45) Date of Patent: Jul. 3, 2001

(54) LOW VOLTAGE DIRECT CONTROL UNIVERSAL PULSE WIDTH MODULATION MODULE

(72) Inventor: Dennis A. Mallier, Racine, WI (US)

(73) Assignee: Transbeta, LLC, Menomonee Falls, WI (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(d) by 0 days.

(21) Appl. No.: 09/344,194

(22) Filed: Jul. 23, 1999

(51) Int. Cl. 7: H02B 47/00

(52) U.S. Cl.: 361/182; 361/84; 361/91.5; 361/187; 361/194; 361/195; 361/196; 361/210

(56) Field of Search: 361/84, 150, 155-157, 361/170, 163.1, 165.1, 187, 194-195, 196-197, 361/210, 152

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4,754,386 6/1998 Baskin et al.
5,717,625 12/1998 Pichard
5,831,763 1/1999 Kozel

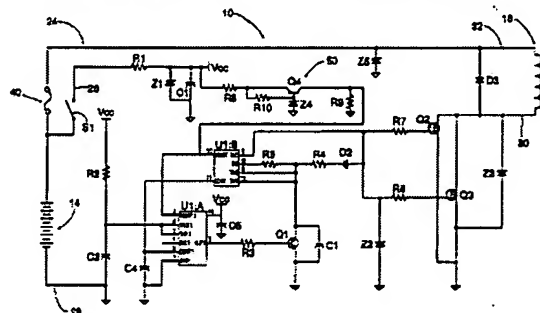
* cited by examiner

Primary Examiner—Michael Sherry
(74) Attorney, Agent, or Firm—Rysz Kromholz & Macdon, S.C.

(57) ABSTRACT

An electrical control module and circuit for controlling a solenoid. The control circuit provides a constant voltage to a solenoid for a predetermined time period after which a pulse width modulated voltage is supplied. The circuit further includes cooperation for reverse polarity protection, transient voltage protection, low gate drive voltage protection, reduced heat dissipation and improved magnetic drive under low input voltage conditions during application of the pulse width modulated voltage. The module may be used in conjunction with a single coil or a dual coil solenoid. The circuit may be utilized on a 12 volt or a 24 volt electrical system without adjustment.

24 Claims, 4 Drawing Sheets



US-PAT-NO: 6256185
DOCUMENT- US 6256185 B1
IDENTIFIER:

See image for Certificate of Correction

TITLE: Low voltage direct control universal pulse width modulation module

Brief Summary Text - BSTX (16):

The circuit preferably includes reverse polarity protection means associated with said first and second voltage control means for opening said circuit in the event that the polarity of said circuit is reversed. A fuse may also be provided, the fuse being sized to open when a reverse polarity condition is detected. In addition, low voltage protection means may be provided for disabling the first and second voltage control means when an inadequate input voltage is supplied to said circuit. The input voltage is preferably in the range of 8 volts to 30 volts.

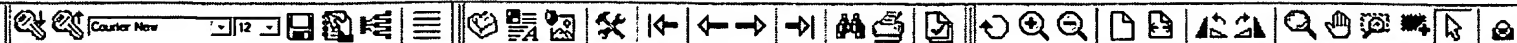
Claims Text - CLTX (14):

8. The circuit of claim 1 further including reverse polarity protection means associated with said first and second voltage control means for opening an external protective fuse wired in series with said circuit in the event that input polarity to said circuit is reversed.

Claims Text - CLTX (36):

21. The circuit of claim 14 further including reverse polarity protection means associated with said voltage supply source for opening a supply fuse in the event that said circuit is reversely connected to said voltage supply source.

Details	Text	Image	HTML	KWIC	
14	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6384875 B2	20020507 18 circuit for a motor vehicle video display positioning system 348/
15	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6256185 B1	20010703 13 Low voltage direct control universal pulse width 361/
16	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6034511 A	20000307 31 light weight rotor and stator with multiple coil 322/



United States Patent (19)

Soda et al.

US 5806621 A
 Patent Number: 5,806,621
 Date of Patent: Sep. 15, 1998

(54) ELECTRIC POWER ASSISTED BICYCLE

(75) Inventors: Hajime Soda; Mamoru Kuroki, both of Sakata, Japan

(73) Assignee: Honda Giken Kogyo Kabushiki Kaisha, Tokyo, Japan

(21) Appl. No.: 476,338

(22) Filed: Jun. 7, 1995

(35) Foreign Application Priority Data

Aug. 18, 1994 (JP) Japan 6-104467

(51) Int. Cl. B60K 28/00

(52) U.S. Cl. 180/206; 180/207; 180/220; 180/654; 180/655

(53) Field of Search 180/220, 205; 180/206, 207, 651, 652, 653, 654, 655, 656, 657, 658, 659, 660, 661, 662, 663, 664, 665, 666, 667, 668, 669, 670, 671, 672, 673, 674, 675, 676, 677, 678, 679, 680, 681, 682, 683, 684, 685, 686, 687, 688, 689, 690, 691, 692, 693, 694, 695, 696, 697, 698, 699, 700, 701, 702, 703, 704, 705, 706, 707, 708, 709, 710, 711, 712, 713, 714, 715, 716, 717, 718, 719, 720, 721, 722, 723, 724, 725, 726, 727, 728, 729, 730, 731, 732, 733, 734, 735, 736, 737, 738, 739, 740, 741, 742, 743, 744, 745, 746, 747, 748, 749, 750, 751, 752, 753, 754, 755, 756, 757, 758, 759, 760, 761, 762, 763, 764, 765, 766, 767, 768, 769, 770, 771, 772, 773, 774, 775, 776, 777, 778, 779, 780, 781, 782, 783, 784, 785, 786, 787, 788, 789, 790, 791, 792, 793, 794, 795, 796, 797, 798, 799, 800, 801, 802, 803, 804, 805, 806, 807, 808, 809, 810, 811, 812, 813, 814, 815, 816, 817, 818, 819, 820, 821, 822, 823, 824, 825, 826, 827, 828, 829, 830, 831, 832, 833, 834, 835, 836, 837, 838, 839, 840, 841, 842, 843, 844, 845, 846, 847, 848, 849, 850, 851, 852, 853, 854, 855, 856, 857, 858, 859, 860, 861, 862, 863, 864, 865, 866, 867, 868, 869, 870, 871, 872, 873, 874, 875, 876, 877, 878, 879, 880, 881, 882, 883, 884, 885, 886, 887, 888, 889, 890, 891, 892, 893, 894, 895, 896, 897, 898, 899, 900, 901, 902, 903, 904, 905, 906, 907, 908, 909, 910, 911, 912, 913, 914, 915, 916, 917, 918, 919, 920, 921, 922, 923, 924, 925, 926, 927, 928, 929, 930, 931, 932, 933, 934, 935, 936, 937, 938, 939, 940, 941, 942, 943, 944, 945, 946, 947, 948, 949, 950, 951, 952, 953, 954, 955, 956, 957, 958, 959, 960, 961, 962, 963, 964, 965, 966, 967, 968, 969, 970, 971, 972, 973, 974, 975, 976, 977, 978, 979, 980, 981, 982, 983, 984, 985, 986, 987, 988, 989, 990, 991, 992, 993, 994, 995, 996, 997, 998, 999, 1000

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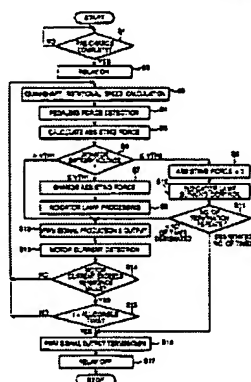
"Yamaha's PAS Power-Assisted Bicycle" *Automotive Engineering* vol. 101 No. 12, Dec. 1993, p. 33.

Primary Examiner—Anna Marie Boulter
 Attorney Agent, or Firm—Blick, Swartz, Kellogg & Block, LLP

ABSTRACT

A secondary battery generates an assisting force for a bicycle. When the voltage of the battery drops, a driver can physically feel a variation in driving feeling to indicate a reduction in battery capacity and so the indicates the amount of capacity remaining. The variation in driving feeling is an increase in force required to pedal the bicycle. In other words, when charging is required, the assisting force which causes pedaling by human power will be reduced. A voltage drop detector senses a variation in the voltage of the battery with certain reference voltage as a voltage drop detection signal. When the voltage drop detection signal is received by an assisting force variation control, a variation assisting force is output to warn the user that battery charge is low. The variation assisting force is obtained by reducing or varying almost to zero an assisting force calculated by an assisting force calculator. A PWM signal processor outputs a PWM signal in response to the assisting force or the variation assisting force to drive a motor of the bicycle.

28 Claims, 12 Drawing Sheets



US-PAT-NO: 5806621

DOCUMENT-IDENTIFIER: US 5806621 A

See image for Certificate of Correction

TITLE: Electric power assisted bicycle

Detailed Description Text - DETX (17):

A heat sensitive resistance element 35 such as a thermistor is disposed between the Ni--Cd batteries 28a and 28b of the upper and lower layers. The heat sensitive resistance element 35 is connected to a charging connector 36 shown in FIG. 3 by way of a lead 35a. Accordingly, when charging is performed using the charger (not shown), the charger side (not shown) can supervise the temperature of the location of the Ni--Cd batteries 28a and 28b from the resistance value of the heat sensitive resistance element 35. Further, various fuses 37 and circuit packs such as a diode (not shown) for preventing a charging voltage of a reverse polarity from being supplied are mounted in the battery case assembly 25.

Detailed Description Text - DETX (30):

The PWM signal production means 104 produces and outputs, from assisting force data A or variation assisting force data HA supplied thereto by way of the assisting force variation warning means 110, a PWM signal 104a necessary to supply an assisting force from the motor 21. The PWM signal 104a is supplied to the gate of a field effect transistor FET by way of a gate driving circuit or the like not shown so that PWM driving of the motor 21 is performed.

Detailed Description Text - DETX (59):

The power source/resetting circuit section 201 includes a 12 V system and 5 V system power source 201a for stepping down the battery power source of, for example, a 24 V system and outputting stabilized power sources of a 12 V system and a 5 V system. Section 201 also includes a resetting circuit 201b which operates with the 5 V system power source. The 12 V system power source is used for the crankshaft rotational speed sensor 75 and as a gate controlling voltage for the field effect transistor FET for power for controlling energization of the motor 21. The 5 V system power source is used by a one-chip microcomputer 203, motor driving limitation means 210, a motor current detection circuit 204 and so forth.

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